

Form PTO-1449 (modified 2/91) INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	U.S. DEPT. OF COMMERCE Patent and Trademark Office		Attorney Docket Number: 2102475-992010	Serial Number: Not yet assigned <i>10/608, 364</i>
	Applicants: Kitagawa, et al.			
	Filing date: June 26, 2003		Group art unit: Not yet assigned <i>2816</i>	

U.S. PATENT DOCUMENTS

Examiner Initial	Patent number	Date	Name	Class	Sub-class	Filing date if appropriate
<i>W</i>	6,307,791	10-23-01	Otsuka, et al.	<i>365</i>	<i>189.05</i>	

FOREIGN PATENT DOCUMENTS

	Document number	Date	Country	Class	Sub-class	Translation YES	NO
<i>W</i>	2001-094048	04-06-01	Japan			X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>W</i>	Kyoung-Hoi Koo, et al., "A New Impedance Control Circuit for USB2.0 Transceiver," ESSCIRC, 2001
<i>M</i>	M. Kumric, et al., "Digitally Tuneable On-chip Line Termination Resistor for 2.5 Gbit/s LVDS Receiver In 0.25 μ m Standard CMOS Technology, ESSCIRC, 10-14-01

Examiner: <i>Wm</i>	Date Considered: <i>4/24/04</i>
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